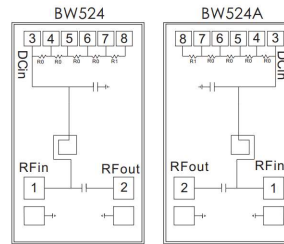


Performance

- Frequency: 1~11GHz
- Insertion loss: 0.1dB@6GHz
- RF/DC Isolation: 36dB@6GHz
- Chock Inductor Parasitic Resistance: 5Ω
- Chip size: 0.65*1.2*0.1mm

Function Diagram



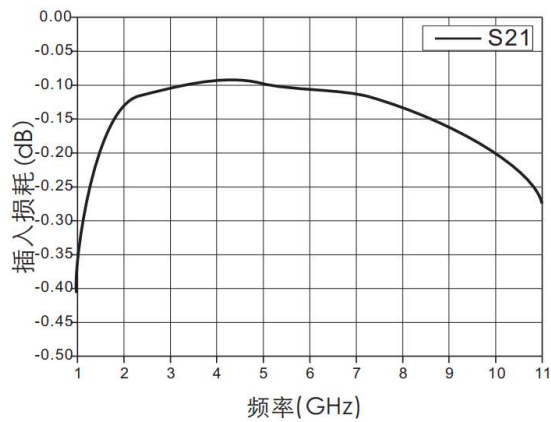
Electrical Specifications (Ta=+25°C, 50Ω system, DC connect to bypass filter capacitor)

Parameter	Test Freq.	Min	Typical	Max	Unit
Insertion Loss	1~11 GHz	-	0.1	-	dB
Input/Output Return loss	1~11 GHz	-	25	-	dB
RF/DC Isolation	1~11 GHz	-	35	-	dB
Chock Inductor Parasitic Resistance	-	-	5	-	Ω

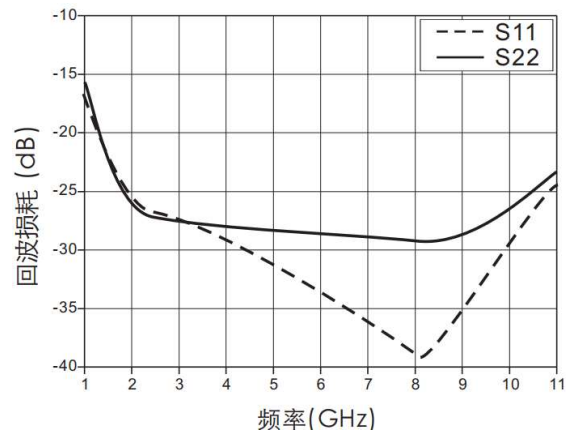
Note: DC/RF isolation is the isolation between pad [3] and [1] [2] when pad [3] connect to 100pF bypass filter capacitor, DC/RF isolation can be improved when enlarge the bypass filter capacitor at pad [3].

Test Curves (Die chip test)

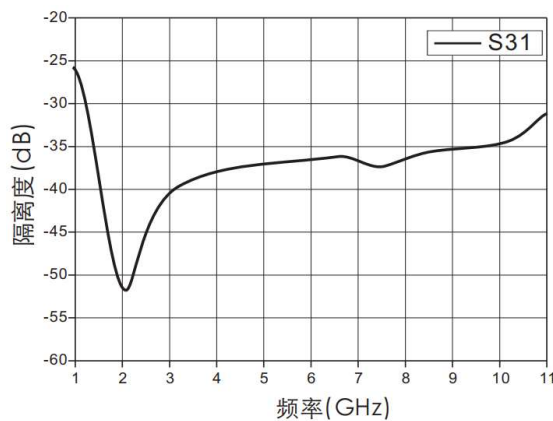
Insertion loss



Return Loss



RF/DC Isolation



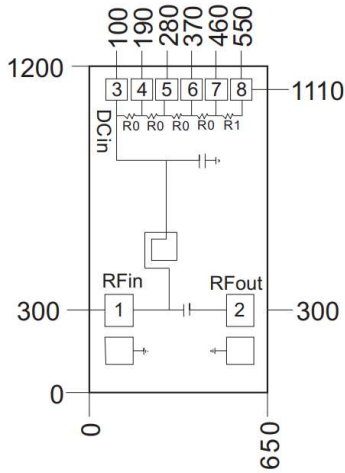
Absolute Max Ratings

Max Input Power (W)	5
DC Current (mA)	150
Storage Temperature	-65 ~ +150°C
Operating Temperature	-55 ~ +125°C
Static Protection Class (HBM)	Class 1A



ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Outline Size



Note:

1. Unit: um
2. Bottom side is gold plated
3. Bottom side is GND
4. Bonding pads is gold plated, [1][2]100*120um [3][4][5][6][7][8]800*800um
5. Don't bonding on thru holds
6. Tolerance: ±50um

Pads Definition

Number	Symbol	Description	Equivalent Circuit
1	RFin	RF input, connect to 50Ω system	
2	RFout	RF output, connect to 50Ω system	
3,4,5,6,7,8	DCin	DC bias port, R0 is 5Ω resistor, R1 is 20Ω resistor, 3~8 series of 0.5,10,15,20,40Ω resistor.	
Bottom	GND	Bottom must be well grounded	

Recommend Drive Circuits	Assembly Diagram									
	<table border="1"> <thead> <tr> <th>No</th> <th>Description</th> <th>Capacitance</th> </tr> </thead> <tbody> <tr> <td>C1</td> <td>Power filter capacitance</td> <td>100~1000pF</td> </tr> <tr> <td>C2</td> <td>Power filter capacitance</td> <td>3.3~22uF</td> </tr> </tbody> </table>	No	Description	Capacitance	C1	Power filter capacitance	100~1000pF	C2	Power filter capacitance	3.3~22uF
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