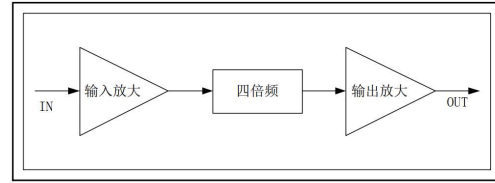


Performance

- Input Frequency: 6.25GHz~10GHz
- Output Frequency: 25GHz~40GHz
- Input Power: 0dBm
- Output Power: 16dBm
- Consumption: 5V/125mA
- Rf3: 20dBc
- Rf5: 20dBc
- Chip size: 1.9*1.2*0.08mm

Function Diagram

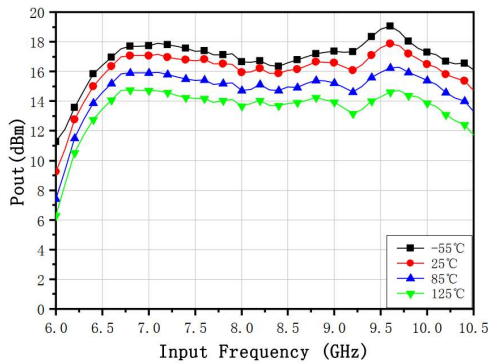


Electrical specifications (TA=+25°C, Pin=0dBm, Vd=5V)

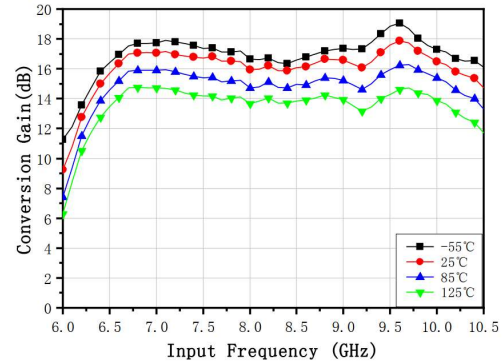
Symbol	Parameter	Min	Typical	Max	Unit
Fin	Input Frequency		6.25~10		GHz
Fout	Output Frequency		25~40		GHz
Pout	Output Power	-	16	-	dBm
Rf3	F3 rejection		20		dBc
Rf5	F5 Rejection	-	20	-	dBc
VSWRin	Input VSWR		1.4		
VSWRout	Output VSWR		2.3		

Test Curves (Pin=5dBm, Vd=5V, Vg=-0.8V)

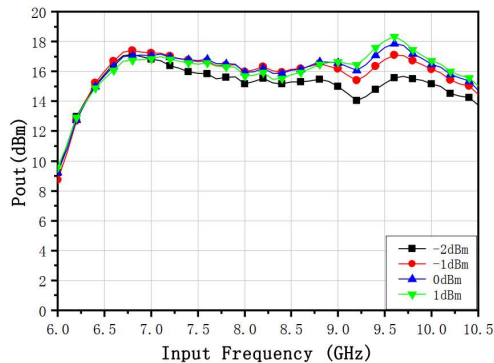
Pout @ Different Temperature



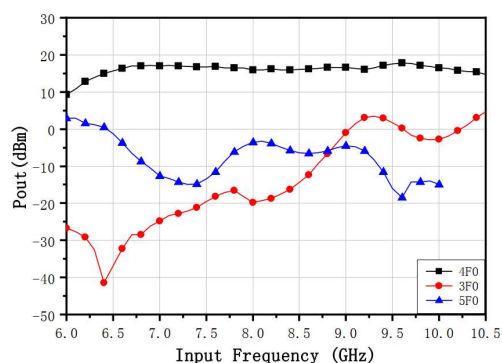
Conversion Gain @ Different Temperature



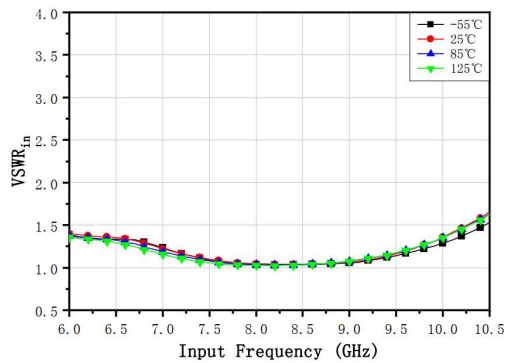
Pout @ Different Pin



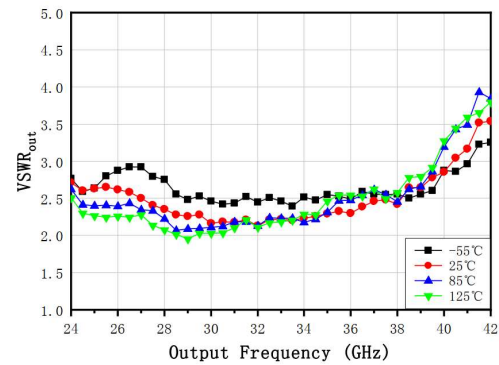
Rejection



VSWR_{in} @ Different Temperature



VSWR_{out} @ Different Temperature

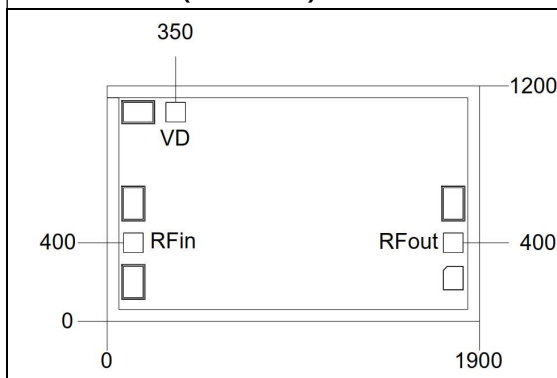


Absolute Max Ratings (TA=25°C)

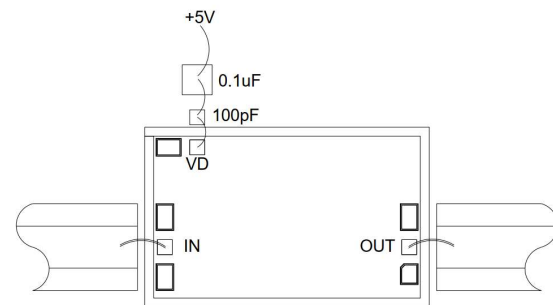
Symbol	Parameter	Value	Note
Vd	Drain Voltage	6V	
Pin	Input Power	20dBm	
Tch	Channel Temperature	175°C	
Tm	Mounting Temperature	310°C	1min, N2 protection
Tstg	Storage Temperature	-65~150°C	

Exceeding any one or combination of these limits may cause permanent damage.

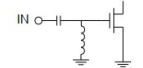
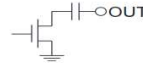
Outline Size (Unit: mm)



Assembly Diagram



Pads Definition

Pad	Description	Equivalent
IN	RF signal input, connect to 50ohm system, no block capacitor needed.	
OUT	RF signal output, connect to 50ohm system, no block capacitor needed.	
VD	Gate bias, external 100pF and 0.1uF capacitor is needed.	