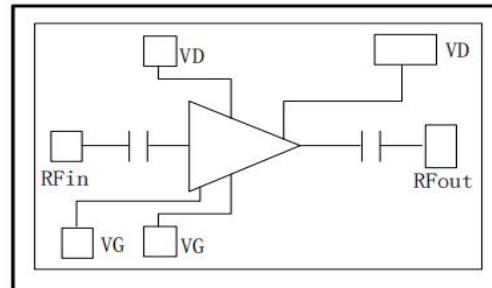


Performance

- Frequency: 2.7~3.5GHz
- Typical Signal Gain: 31dB
- Typical Pout: 45.5dBm@48V
- PAE: 48%
- Bias: 48V, -2V (Typ.)
- Mode: PL
- Technology: 0.35um HEMT
- Size: 3.2mm*2.3mm*0.08mm

Function Diagram

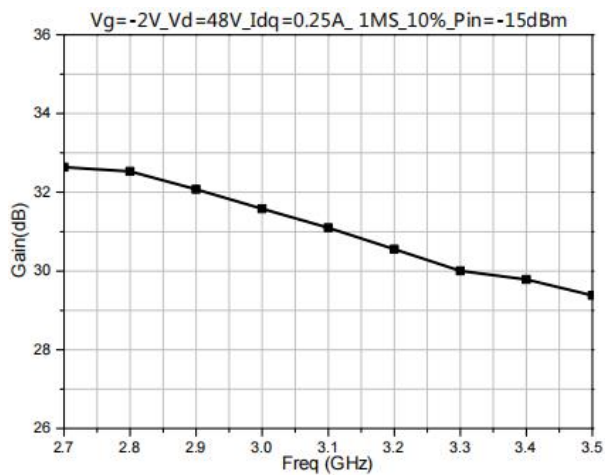


Electrical Specifications (TA=25°C, Vd=48V, F: 2.7~3.5GHz, PW:100us , D.C:10%)

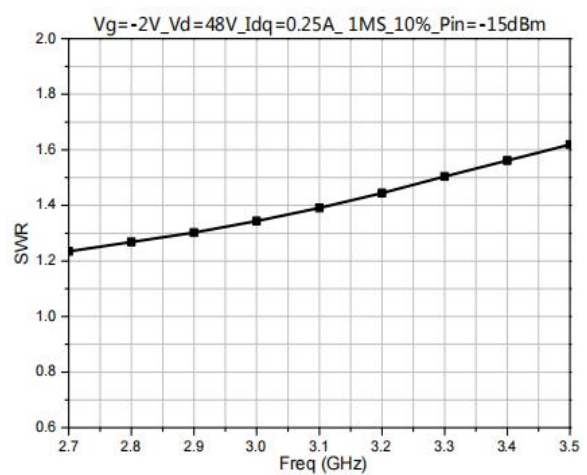
Symbol	Parameter	Min	Typical	Max	Unit
G	Small Signal Gain	-	31	-	dB
Gp	Power Gain	-	23.5	-	dB
Pout	Saturated Power	-	45.5	-	dBm
Id	Operating Current	-	1.65	-	A
PAE	Power Added Efficiency	-	48	-	%

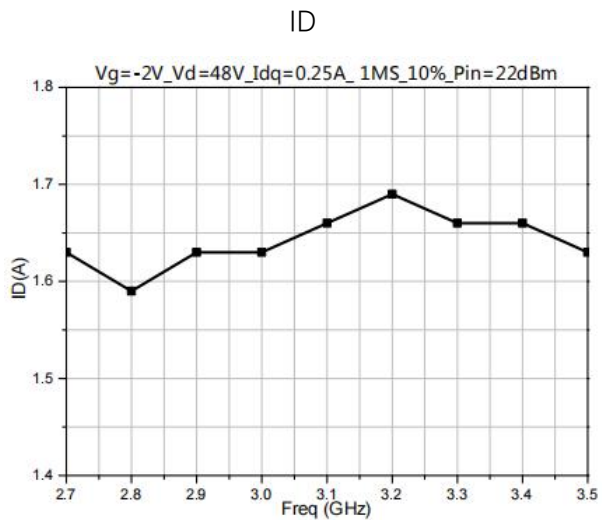
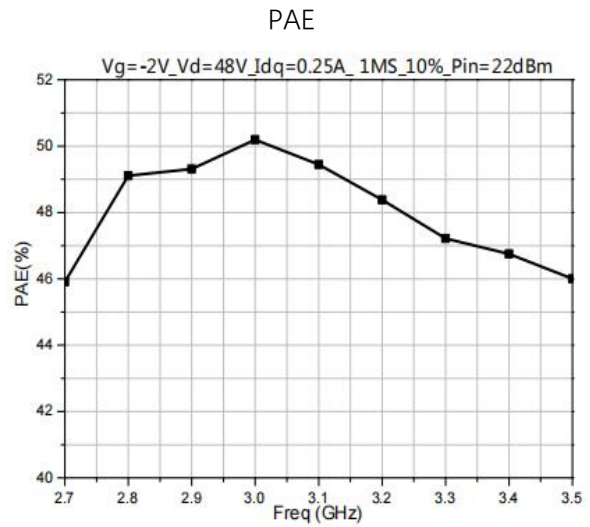
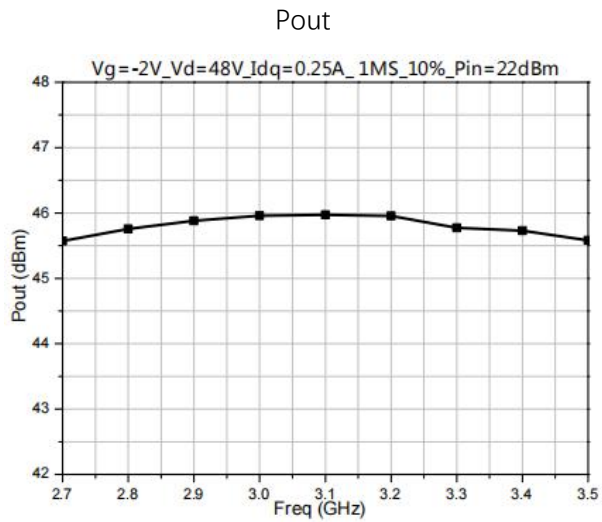
Test Curves

Small Signal Gain



SWR

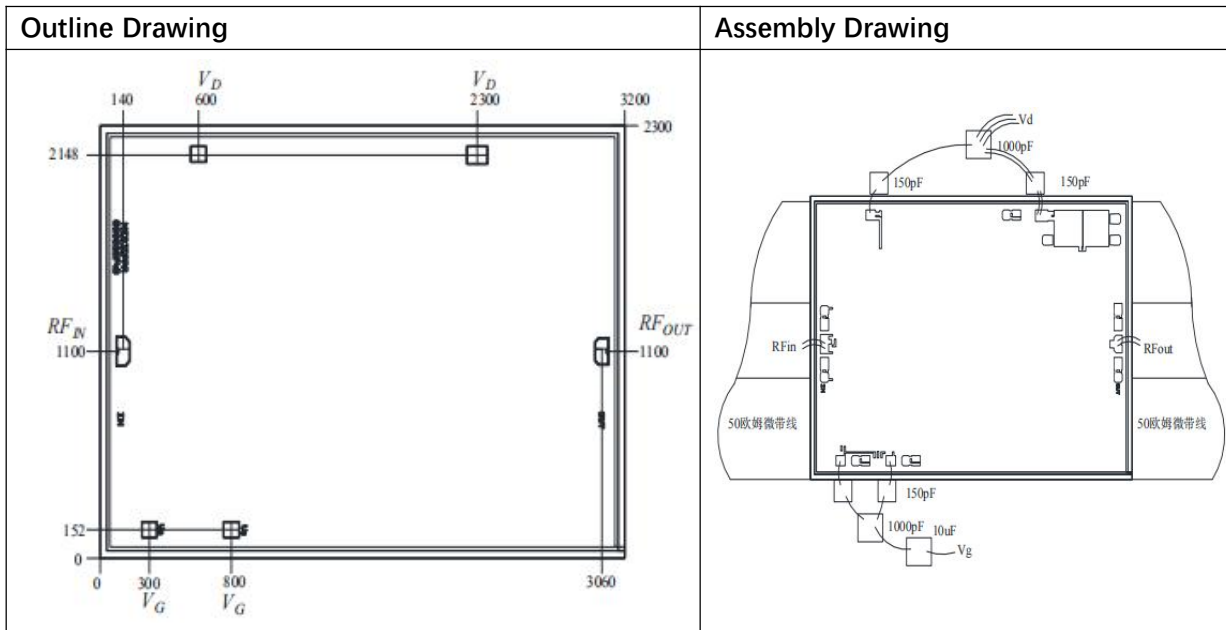




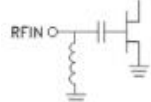

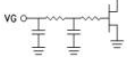

Absolute Max Ratings (TA=25°C)

Symbol	Parameter	Value	Remark
Vd	Drain Voltage	50V	
Vg	Grid Voltage	-10A	
Pd	DC Power	80W	
Pin	Input Power	28dBm	
Tch	Channel Temperature	225°C	
Tm	Mounting Temperature	310°C	30 s, N2 Protection
Tstg	Storage Temperature	-65~150°C	

Exceeding any one or combination of these limits may cause permanent damage.



Pads Definition

Pad	Description	Equivalent Circuit
RFin	RF Signal input, connect to 50ohm system, block capacitor is needed if there's external DC applied on this pad.	
RFout	RF Signal output, connect to 50ohm system, no need block capacitor.	
VG	Amp gate bias, external 150pF, 1000pF, 10uF capacitor is needed	
VD	Amp drain bias, external 150pF, 1000pF capacitor is needed	
GND	Bottom must connect to RF and DC ground	