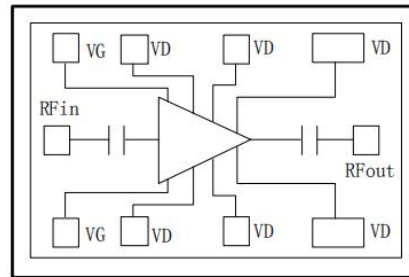


Performance

- Frequency: 50~67GHz
- Typical Signal Gain: 20dB
- Typical Pout: 28dBm@15V
- Typical PAE: 13%
- Bias: 15V, -2V (Typ.)
- Technology: 0.1um GaN
- Size: 3.1*2.2mm*0.05mm

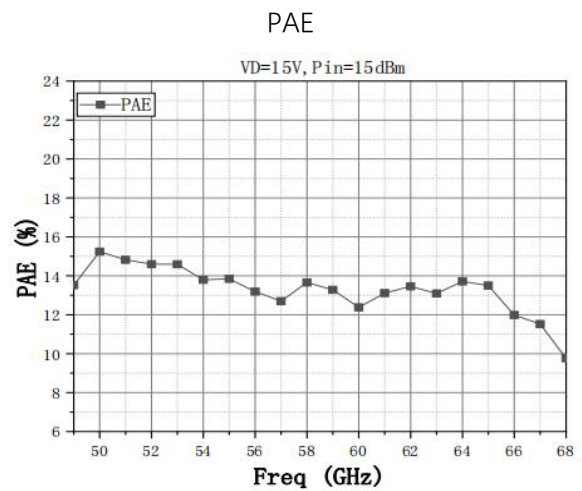
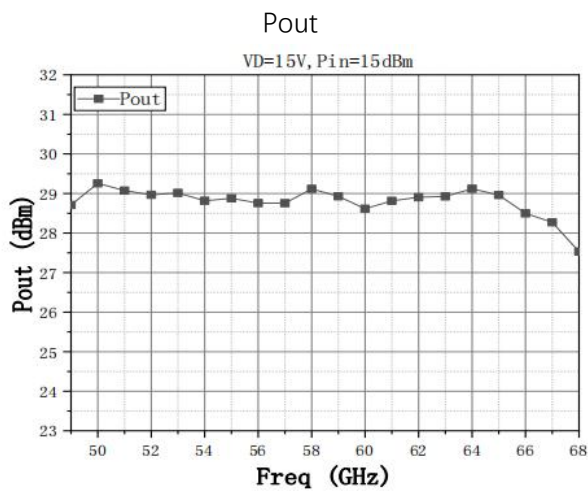
Function Diagram

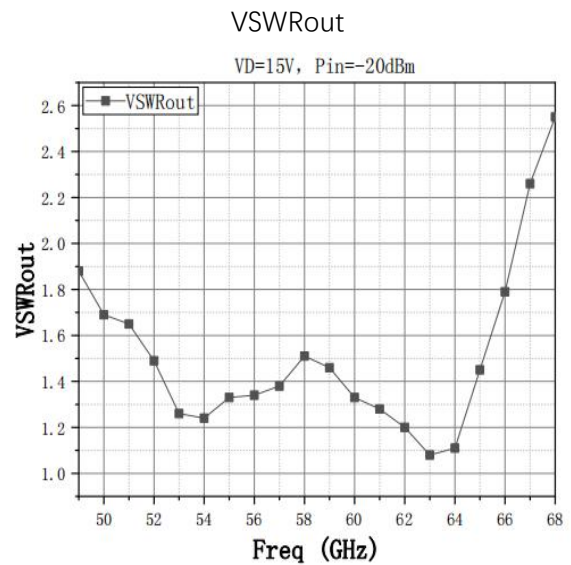
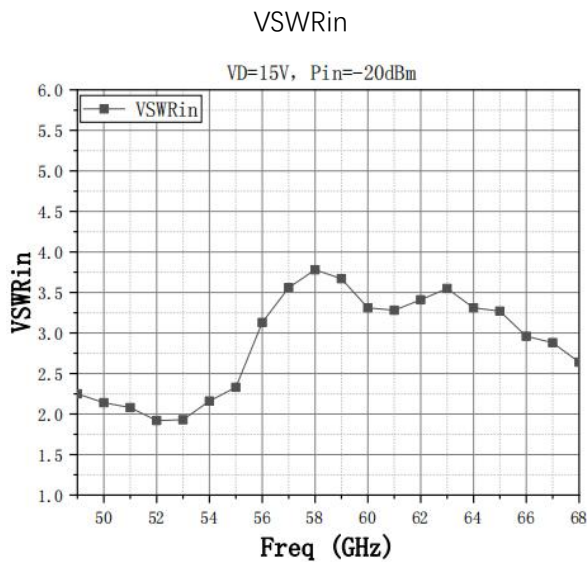
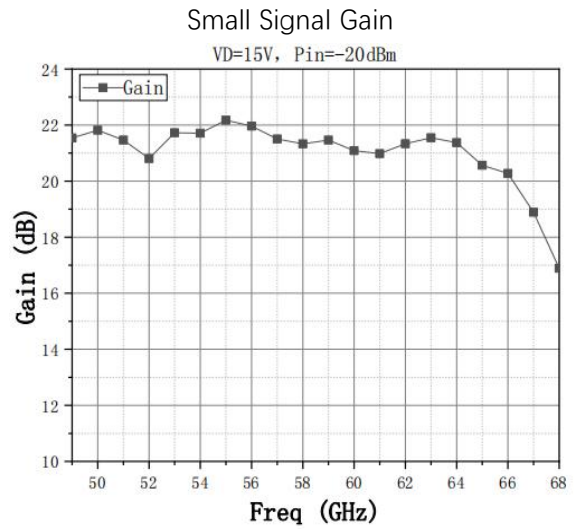
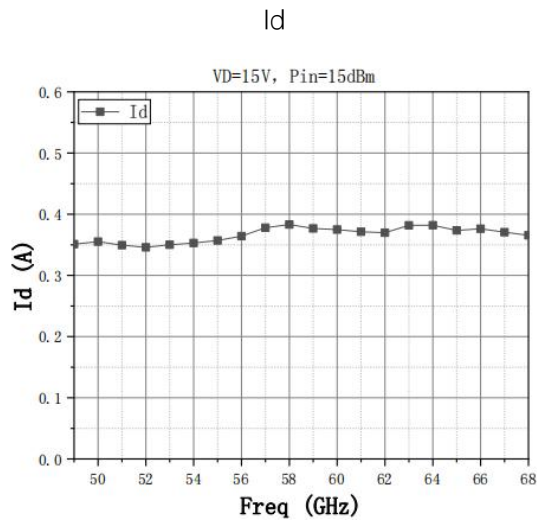


Electrical Specifications (TA=25°C, Vd=15V, Idq=0.3A, F:50~67GHz, CW)

Symbol	Parameter	Min	Typical	Max	Unit
G	Small Signal Gain	-	20	-	dB
Gp	Power Gain	-	13	-	dB
Pout	Saturated Power	-	28	-	dBm
PAE	Power Added Efficiency	-	13	-	%

Test Curves

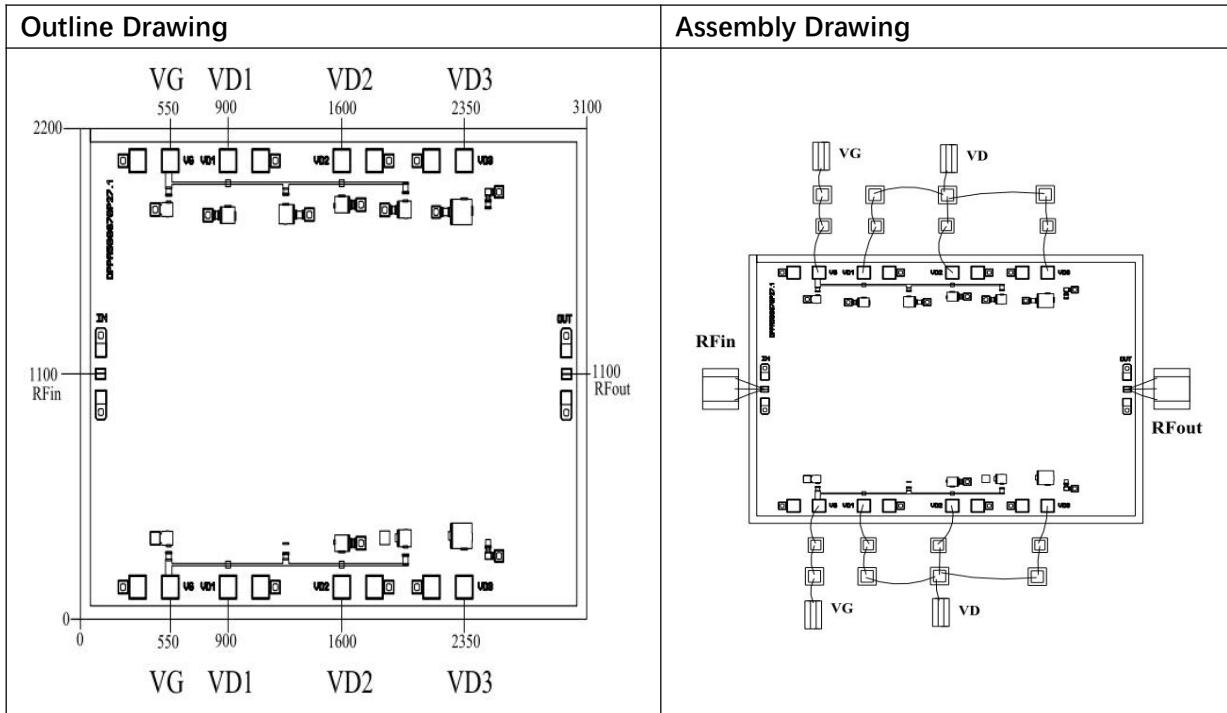




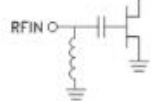

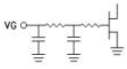

Absolute Max Ratings (TA=25°C)

Symbol	Parameter	Value	Remark
Vd	Drain Voltage	20V	
Id	Drain Current	0.5A	
Vg	Gate Voltage	-10V	
Ig	Gate Current	10mA	
Pd	DC Power	10W	
Pin	Input Power	30dBm	
Tch	Channel Temperature	175°C	
Tm	Mounting Temperature	310°C	1 min, N2 Protection
Tstg	Storage Temperature	-55~165°C	

Exceeding any one or combination of these limits may cause permanent damage.



Pads Definition

Pad	Description	Equivalent Circuit
RFin	RF Signal input, connect to 50ohm system, no need block capacitor.	
RFout	RF Signal output, connect to 50ohm system, no need block capacitor.	
VG	Amp gate bias, external 100uF , 10uF capacitor is needed	
VD1、VD2、VD3	Amp drain bias, external 100uF , 10uF capacitor is needed	
GND	Bottom must connect to RF and DC ground	